

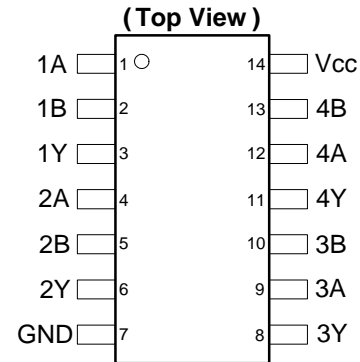
## Description

The 74LVC08A provides four independent 2-input AND gates. The device is designed for operation with a power supply range of 1.65V to 5.5V. The inputs are tolerant to 5.5V allowing this device to be used in a mixed voltage environment. The device is fully specified for partial power down applications using IOFF. The IOFF circuitry disables the output preventing damaging current backflow when the device is powered down.

The gates perform the positive Boolean function:

$$Y = A \bullet B \text{ or } Y = \overline{\overline{A} + \overline{B}}$$

## Pin Assignments



SO-14 / TSSOP-14

SO-14 is a future product

## Features

- Supply Voltage Range from 1.65V to 5.5V
- Sinks 24mA at  $V_{CC} = 3.3V$
- CMOS low power consumption
- IOFF Supports Partial-Power-Down Mode Operation
- Inputs or outputs accept up to 5.5V
- Inputs can be driven by 3.3V or 5.5V allowing for voltage translation applications.
- ESD Protection Exceeds JESD 22
  - 200-V Machine Model (A115-A)
  - 2000-V Human Body Model (A114-A)
  - Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 250mA per JESD 78, Class II
- Range of Package Options
- SO-14<sup>††</sup> and TSSOP-14 Available in “Green” Molding Compound (no Br, Sb)
- Lead Free Finish/ RoHS Compliant (Note 1)

## Applications

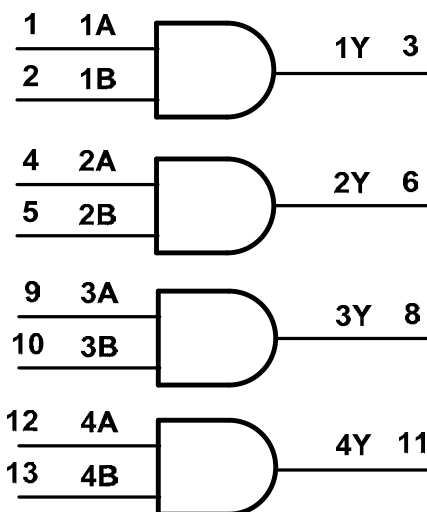
- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide array of products such as:
  - PCs, networking, notebooks, ultrabooks, netbooks
  - Computer peripherals, hard drives, CD/DVD ROM
  - TV, DVD, DVR, set top box

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at [http://www.diodes.com/products/lead\\_free.html](http://www.diodes.com/products/lead_free.html).

**Pin Descriptions**

Pin Number	Pin Name	Description
1	1A	Data Input
2	1B	Data Input
3	1Y	Data Output
4	2A	Data Input
5	2B	Data Input
6	2Y	Data Output
7	GND	Ground
8	3Y	Data Output
9	3A	Data Input
10	3B	Data Input
11	4Y	Data Output
12	4A	Data Input
13	4B	Data Input
14	V <sub>CC</sub>	Supply Voltage

**Logic Diagram**



**Function Table**

Inputs		Output
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

### Absolute Maximum Ratings (Note 2)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V <sub>CC</sub>	Supply Voltage Range	-0.5 to 6.5	V
V <sub>I</sub>	Input Voltage Range	-0.5 to 6.5	V
V <sub>O</sub>	Voltage applied to output in high impedance or I <sub>OFF</sub> state	-0.5 to 6.5	V
V <sub>O</sub>	Voltage applied to output in high or low state	-0.3 to V <sub>CC</sub> +0.5	V
I <sub>IK</sub>	Input Clamp Current V <sub>I</sub> <0	-50	mA
I <sub>OK</sub>	Output Clamp Current V <sub>O</sub> <0	-50	mA
I <sub>O</sub>	Continuous output current	±50	mA
I <sub>CC</sub> , I <sub>GND</sub>	Continuous current through V <sub>CC</sub> or GND	±100	mA
T <sub>J</sub>	Operating Junction Temperature	-40 to 150	°C
T <sub>STG</sub>	Storage Temperature	-65 to 150	°C
P <sub>TOT</sub>	Total Power Dissipation	500	mW

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

### Recommended Operating Conditions (Note 3)

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	Supply Voltage		1.65	5.5	V
V <sub>I</sub>	Input Voltage		0	5.5	V
V <sub>O</sub>	Output Voltage	Active Mode	0	V <sub>CC</sub>	V
		V <sub>CC</sub> = 0V; Power Down Mode	0	5.5	V
Δt/ΔV	Input transition rise or fall rate	V <sub>CC</sub> = 1.65V to 2.7V		20	ns/V
		V <sub>CC</sub> = 2.7V to 5.5V		10	
T <sub>A</sub>	Operating free-air temperature		-40	125	°C

Notes: 3. Unused inputs should be held at V<sub>CC</sub> or Ground.

### Electrical Characteristics

Symbol	Parameter	Test Conditions	Vcc	T <sub>A</sub> = -40°C to 85°C		T <sub>A</sub> = -40°C to 125°C		Unit
				Min	Max	Min	Max	
V <sub>IH</sub>	High-level Input Voltage		1.65V to 1.95V	0.65 X V <sub>CC</sub>		0.65 X V <sub>CC</sub>		V
			2.3V to 2.7V	1.7		1.6		
			2.7V to 3.6V	2.0		2.0		
V <sub>IL</sub>	Low-level input voltage		1.65V to 1.95V		0.35 X V <sub>CC</sub>		0.35 X V <sub>CC</sub>	V
			2.3V to 2.7V		0.7		0.7	
			2.7V to 3.6V		0.8		0.8	
V <sub>OH</sub>	High Level Output Voltage	I <sub>OH</sub> = -100 μA	1.65V to 3.6V	V <sub>CC</sub> - 0.2		V <sub>CC</sub> - 0.3		V
		I <sub>OH</sub> = -4 mA	1.65V	1.2				
		I <sub>OH</sub> = -8 mA	2.3V	1.9				
		I <sub>OH</sub> = -12 mA	2.7V	2.2		2.05		
			3.0V	2.3		2.1		
I <sub>OH</sub> = -24 mA	3.0V	2.2		2.0				
V <sub>OL</sub>	High-level Output Voltage	I <sub>OH</sub> = 100 μA	1.65V to 3.6V		0.2		0.3	V
		I <sub>OH</sub> = 4 mA	1.65V		0.45		0.6	
		I <sub>OH</sub> = 8 mA	2.3V		0.70		0.85	
			2.7V		0.40		0.6	
		I <sub>OH</sub> = -24 mA	3.0V		0.55		0.6	
I <sub>I</sub>	Input Current	V <sub>I</sub> = GND to 5.5V	3.6V		± 5		± 20	μA
I <sub>OFF</sub>	Power Down Leakage Current	V <sub>I</sub> or V <sub>O</sub> = 0V to 3.6V	0		10		20	μA
I <sub>CC</sub>	Supply Current	V <sub>I</sub> = GND or V <sub>CC</sub> I <sub>O</sub> = 0	3.6V		10		40	μA
ΔI <sub>CC</sub>	Additional Supply Current	One input at V <sub>CC</sub> -0.6 V Other at V <sub>CC</sub> or Gnd.	2.7V to 3.6V		500		5000	μA

### Switching Characteristics

Symbol	Parameter	Test Conditions	V <sub>CC</sub>	T <sub>A</sub> = 25°C			-40°C to 85°C		-40°C to 125°C		Unit
				Min	Typ.	Max	Min	Max	Min	Max	
t <sub>PD</sub>	Propagation Delay A <sub>N</sub> or B <sub>N</sub> to Y <sub>N</sub>	Figure 1	1.65V to 1.95V	1.0	5.0	9.3	1.0	9.8	1.0	11.3	ns
			2.3V to 2.7V	1.0	2.9	6.4	1.0	6.9	1.0	9.0	
			2.7V	1.0	3.0	4.6	1.0	4.8	1.0	6.0	
			3V to 3.6V	1.0	2.6	3.9	1.0	4.1	1.0	5.5	
t <sub>SK(0)</sub>	Output Skew Time		3V to 3.6V					1.0		1.5	ns

### Operating Characteristics

T<sub>A</sub> = 25°C

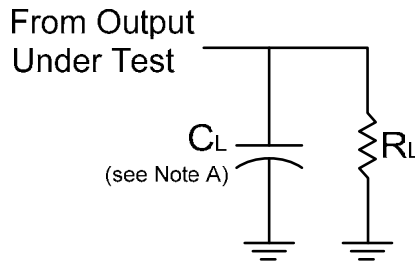
Parameter		Test Conditions	V <sub>CC</sub> = 1.8V	V <sub>CC</sub> = 2.5V	V <sub>CC</sub> = 3.3V	Unit
			Typ.	Typ.	Typ.	
C <sub>pd</sub>	Power dissipation capacitance per gate	f = 10 MHz	7.0	7.5	8.0	pF
C <sub>I</sub>	Input Capacitance	V <sub>i</sub> = V <sub>CC</sub> – or GND	4	4	4	pF

### Package Characteristics

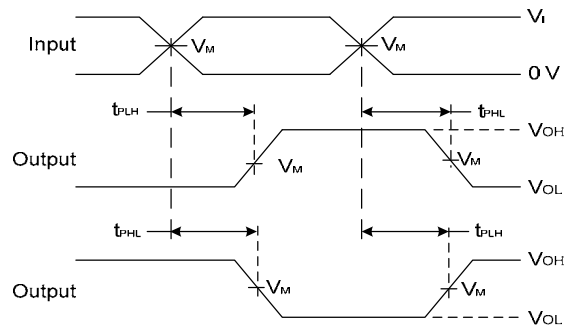
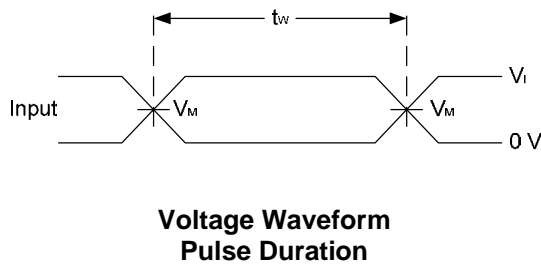
Symbol	Parameter	Test Conditions	V <sub>CC</sub>	Min	Typ.	Max	Unit
θ <sub>JA</sub>	Thermal Resistance Junction-to-Ambient	SO-14 <sup>††</sup>	(Note 4)		TBD		°C/W
		TSSOP-14			159		
θ <sub>JC</sub>	Thermal Resistance Junction-to-Case	SO-14 <sup>††</sup>	(Note 4)		TBD		°C/W
		TSSOP-14			25		

Notes: 4. Test condition for SO-14<sup>††</sup> and TSSOP-14: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

**Parameter Measurement Information**



$V_{CC}$	Inputs		$V_M$	$C_L$	$R_L$
	$V_I$	$t_r/t_f$			
$1.8V \pm 0.15V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	30pF	1K $\Omega$
$2.5V \pm 0.2V$	$V_{CC}$	$\leq 2ns$	$V_{CC}/2$	30pF	500 $\Omega$
2.7V	2.7V	$\leq 2.5ns$	1.5V	50pF	500 $\Omega$
$3.3V \pm 0.3V$	2.7V	$\leq 2.5ns$	1.5V	50pF	500 $\Omega$

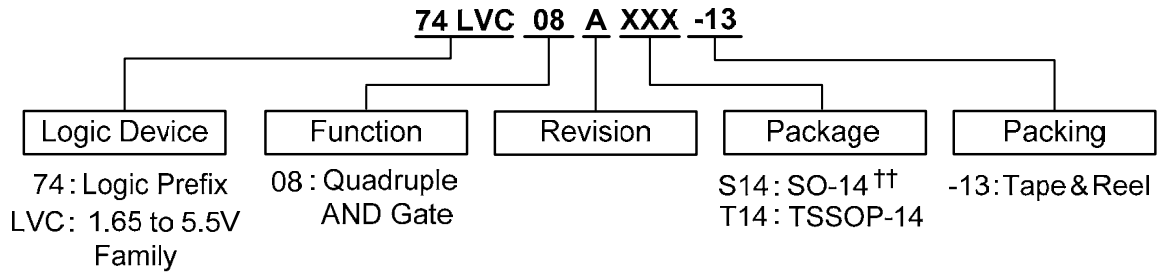


**Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs**

- Notes: A. Includes test lead and test apparatus capacitance.  
 B. All pulses are supplied at pulse repetition rate  $\leq 10$  MHz  
 C. Inputs are measured separately one transition per measurement  
 D.  $t_{PLH}$  and  $t_{PHL}$  are the same as  $t_{PD}$

**Figure 1. Load Circuit and Voltage Waveforms**

**Ordering Information**



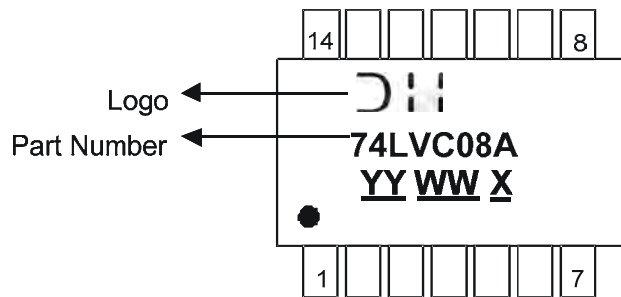
Device	Package Code	Packaging (Note 5)	13" Tape and Reel	
			Quantity	Part Number Suffix
74LVC08AS14-13	S14	SO-14 <sup>††</sup>	2500/Tape & Reel	-13
74LVC08AT14-13	T14	TSSOP-14	2500/Tape & Reel	-13



Notes: 5. The taping orientation and tape details can be found at <http://www.diodes.com/datasheets/ap02007.pdf>

**Marking Information**

(1) SO-14<sup>††</sup>, TSSOP-14

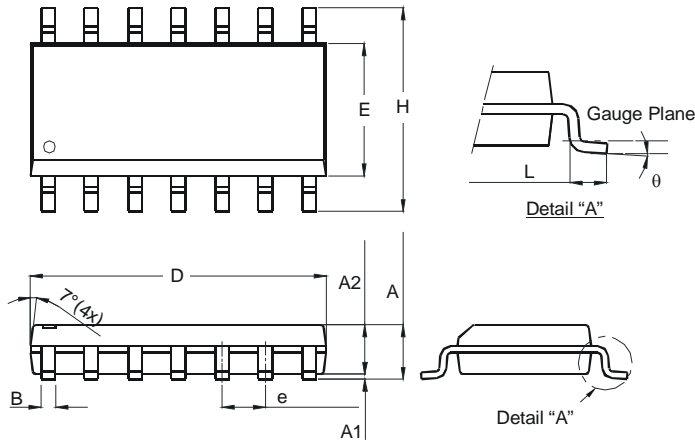


YY : Year : 08, 09, 10~  
WW : Week : 01~52; 52 represents 52 and 53 week  
X : Internal Code

Part Number	Package
74LVC08AS14	SO-14 <sup>††</sup>
74LVC08AT14	TSSOP-14

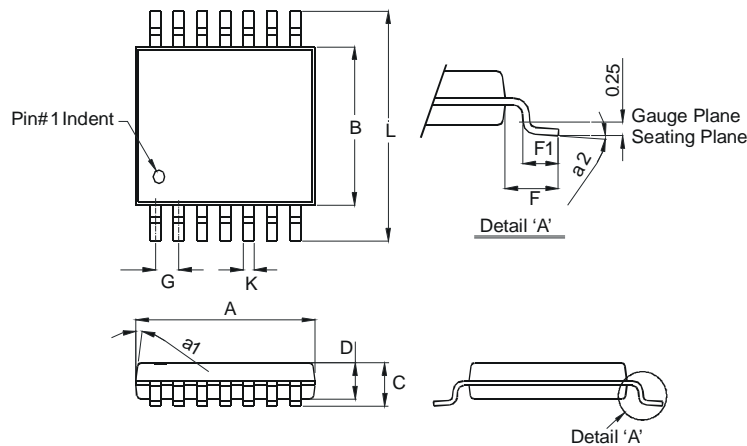
**Package Outline Dimensions (All Dimensions in mm)**

**Package Type: SO-14††**



SO-14††		
Dim	Min	Max
A	1.47	1.73
A1	0.10	0.25
A2	1.45 Typ	
B	0.33	0.51
D	8.53	8.74
E	3.80	3.99
e	1.27 Typ	
H	5.80	6.20
L	0.38	1.27
θ	0°	8°
All Dimensions in mm		

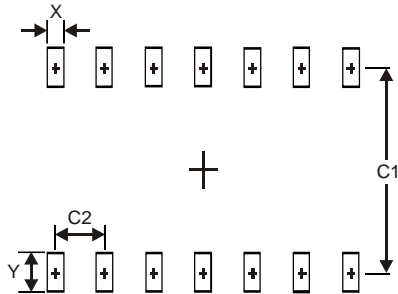
**Package Type: TSSOP-14**



TSSOP-14		
Dim	Min	Max
a1	7° (4X)	
a2	0°	8°
A	4.9	5.10
B	4.30	4.50
C	—	1.2
D	0.8	1.05
F	1.00 Typ	
F1	0.45	0.75
G	0.65 Typ	
K	0.19	0.30
L	6.40 Typ	
All Dimensions in mm		

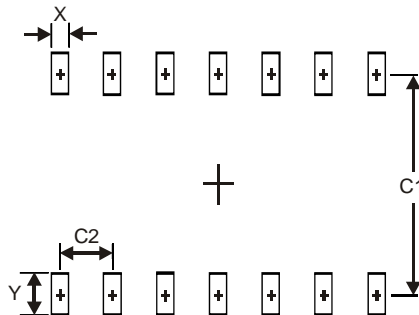
**Suggested Pad Layout**

Package Type: SO-14††



Dimensions	Value (in mm)
X	0.60
Y	1.50
C1	5.4
C2	1.27

Package Type: TSSOP-14



Dimensions	Value (in mm)
X	0.45
Y	1.45
C1	5.9
C2	0.65

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